

**PATENT**

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7/26/05 JCE*

CE filter tap weights with the FFE and DFE filter tap coefficients converged upon by the SE circuit during reception of multiple bursts of training data, and loads these newly calculated tap weight coefficients into the FFE and DFE filters of CE circuit 764 in the RU receiver of Figure 30 via bus 844, as symbolized by step 1132 of Figure 53C.--.

At page 169, line 17, delete "1502" and substitute --1501--.

At page 169, line 21, delete "1504" and substitute --1507--.

At page 169, line 29, delete "1506" and substitute --1509--.

At page 169, line 31, delete "1508" and substitute --1511--.

At page 170, line 2, delete "1510" and substitute --1513--.

At page 170, line 10, delete "1512" and substitute --1515--.

At page 170, line 5, delete "1516" and substitute --1517--.

At page 170, line 12, delete "1514" and substitute --1519--.

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At page 170, line 14, after "." and before "Then", insert --The main tap coefficient of the SE feed forward equalization filter is then set to one and the side tap coefficients of the SE feed forward and decision feedback equalization filters are set to zero for receipt of payload data.--

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At page 187, delete the entire paragraph that extends from line 24 to line 27.

## **IN THE CLAIMS**

**Please cancel claims 1-83 from the parent spec and add the below claims:**

- 1        1. A process carried out to achieve frame synchronization in any digital data communication system having a plurality of physically distributed remote transceivers transmitting frames of the same size on the same frequency on a shared medium to a headend transceiver, comprising the steps:

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the added  
claims 1-13  
have been  
renumbered 84-96*

(a) iteratively transmitting a ranging signal that has correlation